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PATENT
Attorney's Doc. No. 5484-103

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 6,876,585 B2
Inventor: Choi et al.
Issued: April 5, 2005

For: CIRCUIT AND METHOD FOR SELECTING REFERENCE VOLTAGES IN
SEMICONDUCTOR MEMORY DEVICE

Ser. No.: 10/612,593
Group Art Unit: 2818

Filed: July 1, 2003
Examiner: M. Tran

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
AUG 15 2006
of Correction

Enclosed for filing are the following:

- ☒ Request for Certificate of Correction
- ☒ Certificate of Correction Form PTO 1050 (in duplicate)
- ☒ Return postcard
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

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Date: August 8, 2006

Zach Cooper

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REQUEST FOR CERTIFICATE OF CORRECTION

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Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

At column 5, line 59, please replace "signal SI" with --signal S1--
At column 6, line 28, please replace "voltage-VPP" with --voltage VPP--
At column 7, line 24, please replace "voltage" with --voltage.--
At column 10, line 18, please replace "made" with --mode--

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

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DATED : April 5, 2005

INVENTOR(S) : Choi et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

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